

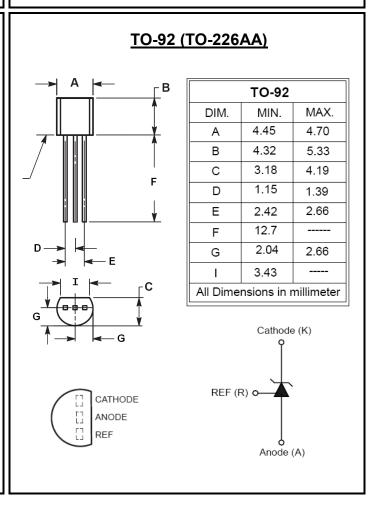
ADJUSTABLE PRECISION SHUNT REGULATOR

GENERAL DESCRIPTION

The LT431 is a low voltage three terminal adjustable shunt regulator with a guaranteed thermal stability over applicable temperature ranges. The output voltage can be set to any value between 2.495V (VREF) to 36V with two external resistors (see application circuit). The high precise Reference voltage tolerance is $\pm 0.5\%$ and $\pm 1.0\%$ by LT431. This device has a typical output impedance of 0.2 Ω . Active output circuitry provides a very sharp turn on characteristic, making this device excel lent replacement for Zener diodes in many applications.

FEATURES

- Precision reference voltage LT431OHPA: 2.495V±0.5% LT431NHPA: 2.495V±1.0%
- Adjustable output voltage is VREF to 36V
- · Sink current capability is 200mA
- Low dynamic output impedance is 0.2Ω (typ.)
- Minimum Cathode current for regulation is 0.2mA (typ.)
- Plastic material has UL flammability classification 94V-0



Absolute Maximum Ratings (at Ta=25°C)

Characteristics	Symbol	Rating	Unit
Cathode Voltage	V_{KA}	36	V
Continuous Cathode Current	I _{KA}	250	mA
Reference Input Current	I _{REF}	10	mA
Operating Temperature	T _{OP}	-20~85	$^{\circ}\!\mathbb{C}$
Junction Temperature	T _J	-40~125	$^{\circ}\!\mathbb{C}$
Storage Temperature	T _{STG}	-40~150	$^{\circ}\!\mathbb{C}$
Thermal Resistance from Junction to ambient	θ JA	156	°C/W
Power Dissipation[PD=(TJ-TA) / θJA]	PD	0.25	W

Note: θ JA is measured with the PCB copper area of approximately 1 in2(Multi-layer).

Rev.1, Apr-2009, KTZD01

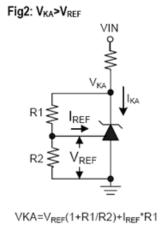


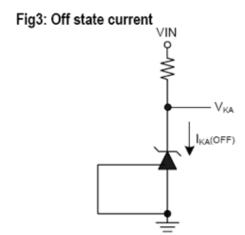
Electrical Characteristics (T_A=25°C, unless otherwise specified)

Characteristics	Symbol	Conditions		Min	Тур	Max	Unit	
Reference Voltage	V_{REF}	V _{KA} = V _{REF,} I _{KA} = 10mA (Fig.1)		0.5 %	2.482	2.495	2.507	V
				1.0 %	2.470	2.495	2.520	
Deviation of Reference Input Voltage over full temperature range		$V_{KA} = V_{REF}, I_{KA} = 10\text{mA},$ $T_A = -20 \sim 85^{\circ} \text{C (Fig.1)}$			6.0	20	mV	
Reference Input Current	I _{REF}	R1 = 10KΩ,R2 = ∞ I _{KA} = 10mA (Fig.2)				1.5	3.5	uA
Deviation of Reference Input Current over Temperature		R1 = 10KΩ,R2 = ∞ I _{KA} = 10mA T _A = -20~85°C (Fig.2)			0.4	1.2	uA	
Ratio of the Change in Reference V _{KA} = 10V ~V _{REF} 1.2 -2.0		F IKA - TOTTIA TELE		IOV ~V _{REF}		-1.2	-2.0	mV/V
Voltage to the Change in Cathode Voltage	ΔV_{KA}			6V ~10V		-1	-2.0	
Minimum Cathode Current for Regulation	I _{KA(min)}	$V_{KA} = V_{REF}$ (Fig.1)			0.2	0.5	mA	
Off-state Cathode Current	I _{KA(OFF)}	$V_{KA} = 36V, V_{REF} = 0V (Fig.3)$			0.1	1	uA	
Dynamic Output Impedance	Z _{KA}	V _{KA} = V _{REF} Frequency ≤ 1KHz (Fig.1)				0.2	0.5	Ω

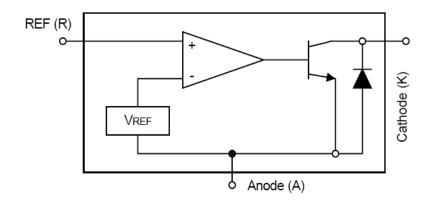
Application Circuit

Fig1: V_{KA}=V_{REF}



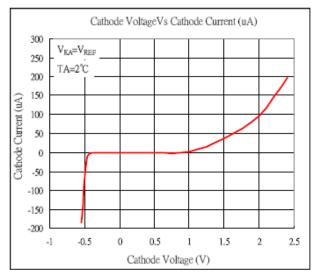


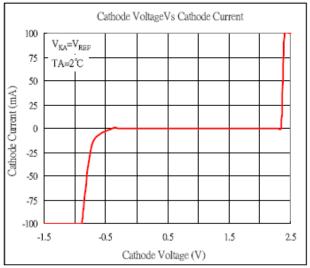
Block Diagram

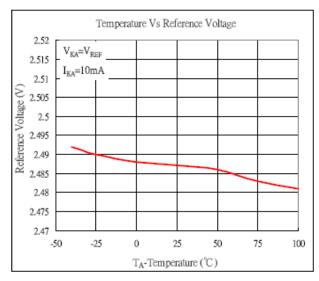


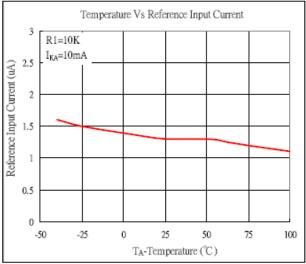


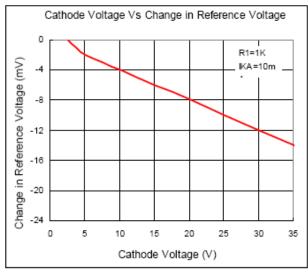
Typical Characteristics







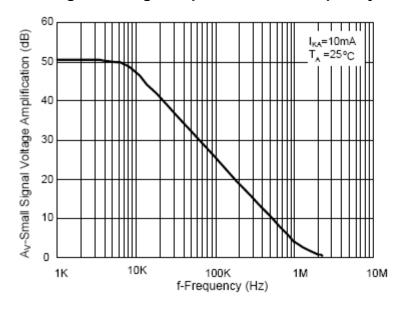


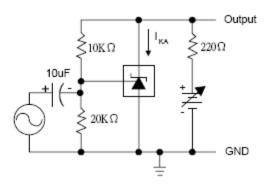




Typical Characteristics (Continued)

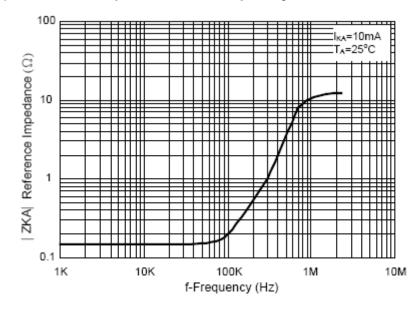
(1) Small Signal Voltage Amplification Vs Frequency

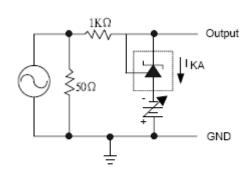




TEST CIRCUIT FOR VOLTAGE AMPLIFICATION

(2) Reference Impedance VS Frequency



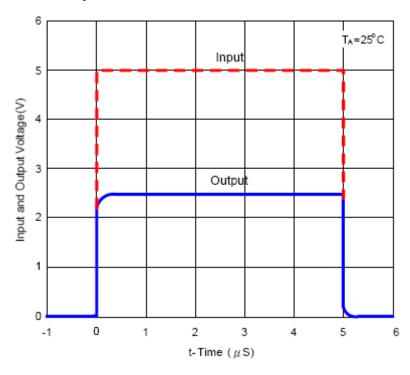


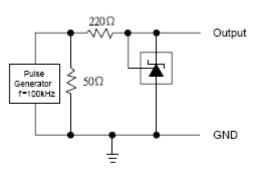
TEST CIRCUIT FOR REFERENCE IMPEDANCE



Typical Characteristics (Continued)

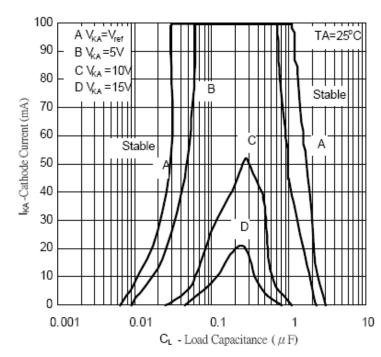
(3) Pulse Response

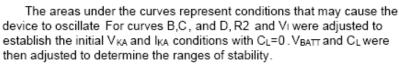


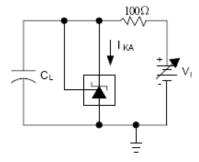


TEST CIRCUIT FOR PULSE RESPONSE

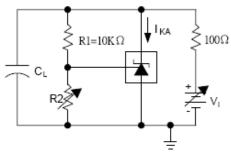
(4) Stability boundary conditions







TEST CIRCUIT FOR CURVE A



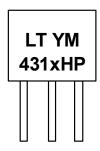
TEST CIRCUIT FOR CURVE B, C, AND D



Ordering Information

Product Number	Output Voltage Tolerance	Package	Packing
LT4310HPA	0.5 %	TO-92	Taping & Reel
LT431NHPA	1.0 %	TO-92	Taping & Reel

Marking Information



Note:

1) YM = Date Code, Y: Year, M: Month

2) 431**x**HP = Product Number

x = O, N...

O= 0.5 %

N= 1.0 %